

PCI-X Development Board

1. Overview



The Terasic Cyclone® III PCI-X development board provides a hardware platform for developing and prototyping low-power, high-performance, logic-intensive PCI-based designs. The board has a high-density of the external memory to facilitate the design and development of FPGA designs which need huge storage capacity and high bandwidth memory. It also includes Low-Voltage Differential Signaling (LVDS) interface of the High-Speed Terasic Connectors (HSTCs) for extra high-speed interface application.

To simplify the design process, we provide the software PCI System Builder that supplies a convenient way to build interfaces between host PC and user logic on FPGA, and also contains the interface of multi-port memory controller which allows shared access to a unique external memory.

2. Specification

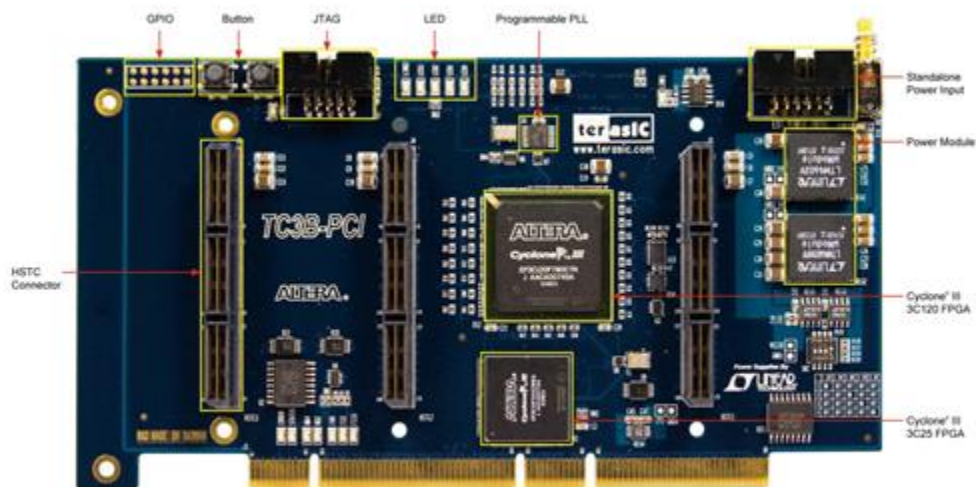
- Altera Cyclone® III 3C120 FPGA device.
 - 119,088 logic elements (LEs)
 - 3,981,312 total RAM bits
 - 288 18 x 18 multiplier blocks
 - Altera Cyclone® III 3C25 FPGA device.
 - EPCS64 and EPCS16 Configuration Device
 - Up to 4GB DDR2 SO-DIMM
 - Support 120 Low Voltage Differential Signal (LVDS) pairs.
 - Clock Source
-
- **Company: Terasic**
 - **Tel: +886-3-5750880**
 - **Fax: +886-3-5726690**
 - **Add: 9F., No.176, Sec.2, Gongdao 5th Rd, East Dist, Hsinchu City, 30070. Taiwan**

- 100Mhz Osc.
 - 80Khz to 200Mhz Programmable PLL
- Selectable IO Voltage (1.2V / 1.5V / 1.8V/ 2.5V / 3.3V)
- Powerful Software - PCI System Builder
- Complete Reference Designs

3. Kit content

- Cyclone III PCI development board
- Terasic USB Blaster
- USB Cable for FPGA programming and control
- CD-ROM containing the User Manual, the Control Panel utility, the PCI SystemBuilder and reference designs.
- THDB_HLB
- THDB_HFF
- Screw and Copper Pillar Package
- Power Cable

4. Layout



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- Size: 100.8x190mm

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